

INTERVAL	TIMING FUNCTION	Ø1	Ø2	Ø3	Ø4	Ø5	Ø6	Ø7	Ø8
0	WALK		7				7	7	7
1	FLASHING DON'T WALK		22				23	23	25
2	MINIMUM INITIAL	6	10			6	10	6	6
3	TYPE 3 DET. DISCONNECT	0	16			0	16	0	0
4	ADDED SEC./ACTUATION	0	1.5			0	1.5	0	0
5	PASSAGE	2	4			2	4	2	2
6	MAXIMUM GAP	3	5			3	5	3	3
7	MINIMUM GAP	1	3			1	3	1.5	1.5
8	MAXIMUM EXTENSION I	21	20			16	20	16	21
9	MAXIMUM EXTENSION II	26							
A	MAXIMUM EXTENSION III								
B									
C	SEC. OF GAP REDUCED	0.1	0.1			0.1	0.1	0.1	0.1
D	PER SEC. OF INTERVAL	0.8	1.5			0.8	1.5	0.8	1
E	YELLOW	3	4			3	4	3.2	3.2
F	RED CLEARANCE	0	0			0	0	1	1

TURN ON 1715EB/W	TIMING CHANGE BY: SC	REMARKS <b>ALL RED FLASH</b>	FILE
DATE May 20, '93	DATE July 27, '09	Print Date July 27, '09	By SC
		FILENAME Hickey_Gellert_C8.xls	E# 5Ø
COUNTY <b>SM</b>	ROUTE <b>280</b>	PM <b>PM</b>	CITY <b>Daly City</b>
		INTERSECTION <b>HICKEY BLVD. &amp; GELLERT BLVD. (CITY)</b>	
		PROGRAM <b>C8.4 Ld/Lg</b>	
NOTE: To initialize Controller: 1) Set Location & Feature Switches; 2) Clear RAM Location C-C-0 with STOP-TIME ON; 3) Enter Non-zero at C-C-1 to enter timing; 4) Enter 0 at C-C-1 to start <b>***SET REAL TIME CLOCK TO TELEPHONE TIME***</b>			
* MODEM REQUIRED; MASTER @ RTE 280 NB RAMPS & HICKEY BLVD. * *** Set Phase 3 timing the same as phase 8 timings for proper operation of EV Updates to coordination plans per implementation/fine-tuning - 11/2008.			

INTERVAL	FLAG FUNCTION	DISPLAY	Ø1	Ø2	Ø3	Ø4	Ø5	Ø6	Ø7	Ø8
0	PERMITTED PHASES	F 243	ON	ON			ON	ON	ON	ON
1	RED DETECTOR LOCK									
2	YELLOW DET. LOCK	F 017	ON				ON			
3	VEHICLE RECALL	F 034		ON				ON		
4	PEDESTRIAN RECALL									
5	PEDESTRIAN PHASES	F 226		ON				ON	ON	ON
6	OVERLAP A									
7	OVERLAP B									
8	DOUBLE ENTRY	F 034		ON				ON		
9	MAX EXT. II	F 001	ON							
A	LAG PHASES	VIEW	<b>FOR OBSERVATION ONLY (SET LAG PHASES AT C-F-0 TO C-F-9)</b>							
B	RED REST									
C	NON ACTUATED									
D	MAXIMUM EXT. III									
E	START UP YELLOW									
F	FIRST PHASE GREEN	F 034		ON				ON		

EPROM BOARD - 412C																		
CHIP	PROGRAM	NUMBER	CHIP	CHECKSUM	PROGRAM	NUMBER	CHECKSUM	CHECKSUM					ENTER	FUNCTION	CODE	ENTER	DISPLAY	
U1	C8.4	C#69	U2	28D4	C8.4	D#69	O6CE	1	2	3	4	5	6	7	8			
	LOCATION (1=ON)	1	2	3	4	5	6	7	8	FEATURE (1=ON)						D-3-C	20 E	20.0
	SWITCH (0=OFF)	1	1	0	0	0	0	0	0	SWITCH (0=OFF)						D-3-D	20 E	20.0
CODE	FUNCTION																	
F-0-E	MAXIMUM VARIABLE INITIAL																	
F-0-F	RED REVERT																	
F-D-0	TBCSEL																	
F-D-1	HOUR																	
F-D-2	MINUTE																	
F-D-8	OFFSET SEEKING FLAG																	
C-0-0	LOCAL ADDRESS																	
C-C-2	PC MASTER DOWNLOAD																	
C-F-C	COORDINATED FAZES																	
D-0-9	FEATURE (Set by Feature Switch)																	
* F-C-F	RAM ACCESS ( Set/Clear)																	
* E-C-D	ASSIGN DETECTOR 319L AS 119L																	
* E-F-4	Assign 63L as calling type																	
* F-C-F	RAM ACCESS ( Set/Clear)																	
* E-F-7	Ph 7 Ped Output to Ph 4 Ped L/S																	
F-E-8	EVD Delay																	
F-E-9	EVD Hold																	
F-E-A	EV MAX TIMER																	
C-F-0	LAG FAZES "FREE"																	
C-F-1	LAG FAZES "PATTERN 1"																	
C-F-2	LAG FAZES "PATTERN 2"																	
C-F-3	LAG FAZES "PATTERN 3"																	
C-F-4	LAG FAZES "PATTERN 4"																	
C-F-5	LAG FAZES "PATTERN 5"																	
C-F-6	LAG FAZES "PATTERN 6"																	
C-F-7	LAG FAZES "PATTERN 7"																	
C-F-8	LAG FAZES "PATTERN 8"																	
C-F-9	LAG FAZES "PATTERN 9"																	

SM 280 0 PM  
 County Route

HICKEY BLVD. & GELLERT BLVD. (CITY)  
 Location

Daily City  
 City

PATTERN 1			
CODE	FUNCTION	ENTER	DISPLAY
C-1-0	CYC. LENG.	100 E	C 100
C-1-1	φ 1 SPLIT	20 E	C 020
C-1-2	φ 2 SPLIT	E	C
C-1-3	φ 3 SPLIT	E	C
C-1-4	φ 4 SPLIT	E	C
C-1-5	φ 5 SPLIT	14 E	C 014
C-1-6	φ 6 SPLIT	E	C
C-1-7	φ 7 SPLIT	19 E	C 019
C-1-8	φ 8 SPLIT	21 E	C 021
C-1-A	OFFSET A	0 E	C 000
C-1-B	OFFSET B	E	C
C-1-C	OFFSET C	E	C

PATTERN 4			
CODE	FUNCTION	ENTER	DISPLAY
C-4-0	CYC. LENG.	120 E	C 120
C-4-1	φ 1 SPLIT	21 E	C 021
C-4-2	φ 2 SPLIT	E	C
C-4-3	φ 3 SPLIT	E	C
C-4-4	φ 4 SPLIT	E	C
C-4-5	φ 5 SPLIT	15 E	C 015
C-4-6	φ 6 SPLIT	E	C
C-4-7	φ 7 SPLIT	31 E	C 031
C-4-8	φ 8 SPLIT	27 E	C 027
C-4-A	OFFSET A	67 E	C 067
C-4-B	OFFSET B	E	C
C-4-C	OFFSET C	E	C

PATTERN 7			
CODE	FUNCTION	ENTER	DISPLAY
C-7-0	CYC. LENG.	110 E	C 110
C-7-1	φ 1 SPLIT	20 E	C 020
C-7-2	φ 2 SPLIT	E	C
C-7-3	φ 3 SPLIT	E	C
C-7-4	φ 4 SPLIT	E	C
C-7-5	φ 5 SPLIT	9 E	C 009
C-7-6	φ 6 SPLIT	E	C
C-7-7	φ 7 SPLIT	31 E	C 031
C-7-8	φ 8 SPLIT	25 E	C 025
C-7-A	OFFSET A	45 E	C 045
C-7-B	OFFSET B	E	C
C-7-C	OFFSET C	E	C

COORD MAX RECALL			
CODE	PATTERN	ENTER	CALL LAMPS
D-D-1	1		d
D-D-2	2		d
D-D-3	3		d
D-D-4	4		d
D-D-5	5		d
D-D-6	6		d
D-D-7	7		d
D-D-8	8		d
D-D-9	9		d

PATTERN 2			
CODE	FUNCTION	ENTER	DISPLAY
C-2-0	CYC. LENG.	100 E	C 100
C-2-1	φ 1 SPLIT	17 E	C 017
C-2-2	φ 2 SPLIT	E	C
C-2-3	φ 3 SPLIT	E	C
C-2-4	φ 4 SPLIT	E	C
C-2-5	φ 5 SPLIT	14 E	C 014
C-2-6	φ 6 SPLIT	E	C
C-2-7	φ 7 SPLIT	19 E	C 019
C-2-8	φ 8 SPLIT	21 E	C 021
C-2-A	OFFSET A	0 E	C 000
C-2-B	OFFSET B	E	C
C-2-C	OFFSET C	E	C

PATTERN 5			
CODE	FUNCTION	ENTER	DISPLAY
C-5-0	CYC. LENG.	110 E	C 110
C-5-1	φ 1 SPLIT	17 E	C 017
C-5-2	φ 2 SPLIT	E	C
C-5-3	φ 3 SPLIT	E	C
C-5-4	φ 4 SPLIT	E	C
C-5-5	φ 5 SPLIT	12 E	C 012
C-5-6	φ 6 SPLIT	E	C
C-5-7	φ 7 SPLIT	23 E	C 023
C-5-8	φ 8 SPLIT	25 E	C 025
C-5-A	OFFSET A	E	C 000
C-5-B	OFFSET B	E	C
C-5-C	OFFSET C	E	C

PATTERN 8			
CODE	FUNCTION	ENTER	DISPLAY
C-8-0	CYC. LENG.	120 E	C 120
C-8-1	φ 1 SPLIT	24 E	C 024
C-8-2	φ 2 SPLIT	E	C
C-8-3	φ 3 SPLIT	E	C
C-8-4	φ 4 SPLIT	E	C
C-8-5	φ 5 SPLIT	18 E	C 018
C-8-6	φ 6 SPLIT	E	C
C-8-7	φ 7 SPLIT	31 E	C 031
C-8-8	φ 8 SPLIT	21 E	C 021
C-8-A	OFFSET A	35 E	C 035
C-8-B	OFFSET B	E	C
C-8-C	OFFSET C	E	C

COORD MIN RECALL			
CODE	PATTERN	ENTER	CALL LAMPS
D-E-1	1		d
D-E-2	2		d
D-E-3	3		d
D-E-4	4		d
D-E-5	5		d
D-E-6	6		d
D-E-7	7		d
D-E-8	8		d
D-E-9	9		d

PATTERN 3			
CODE	FUNCTION	ENTER	DISPLAY
C-3-0	CYC. LENG.	110 E	C 110
C-3-1	φ 1 SPLIT	25 E	C 025
C-3-2	φ 2 SPLIT	E	C
C-3-3	φ 3 SPLIT	E	C
C-3-4	φ 4 SPLIT	E	C
C-3-5	φ 5 SPLIT	25 E	C 025
C-3-6	φ 6 SPLIT	E	C
C-3-7	φ 7 SPLIT	24 E	C 024
C-3-8	φ 8 SPLIT	21 E	C 021
C-3-A	OFFSET A	57 E	C 057
C-3-B	OFFSET B	E	C
C-3-C	OFFSET C	E	C

PATTERN 6			
CODE	FUNCTION	ENTER	DISPLAY
C-6-0	CYC. LENG.	120 E	C 120
C-6-1	φ 1 SPLIT	22 E	C 022
C-6-2	φ 2 SPLIT	E	C
C-6-3	φ 3 SPLIT	E	C
C-6-4	φ 4 SPLIT	E	C
C-6-5	φ 5 SPLIT	12 E	C 012
C-6-6	φ 6 SPLIT	E	C
C-6-7	φ 7 SPLIT	23 E	C 023
C-6-8	φ 8 SPLIT	25 E	C 025
C-6-A	OFFSET A	E	C 000
C-6-B	OFFSET B	E	C
C-6-C	OFFSET C	E	C

PATTERN 9			
CODE	FUNCTION	ENTER	DISPLAY
C-9-0	CYC. LENG.	120 E	C 120
C-9-1	φ 1 SPLIT	25 E	C 025
C-9-2	φ 2 SPLIT	E	C
C-9-3	φ 3 SPLIT	E	C
C-9-4	φ 4 SPLIT	E	C
C-9-5	φ 5 SPLIT	11 E	C 011
C-9-6	φ 6 SPLIT	E	C
C-9-7	φ 7 SPLIT	31 E	C 031
C-9-8	φ 8 SPLIT	33 E	C 033
C-9-A	OFFSET A	51 E	C 051
C-9-B	OFFSET B	E	C
C-9-C	OFFSET C	E	C

COORD RED RECALL			
CODE	PATTERN	ENTER	CALL LAMPS
D-F-1	1		d
D-F-2	2		d
D-F-3	3		d
D-F-4	4		d
D-F-5	5		d
D-F-6	6		d
D-F-7	7		d
D-F-8	8		d
D-F-9	9		d

SM 280 0 PM  
 County Route

HICKEY BLVD. & GELLERT BLVD. (CITY)  
 LOCATION

Daly City  
 CITY

CONTROL CODE "7"												
TIME OF DAY ACTIVITY TABLE												
KEY STROKES 7 + EVENT # + HOUR + MIN + ACT CODE + "E" + ON/OFF + DOW LTS												
EVENT #	TIME	ACTIVITY CODE	DMPRESS	ON/OFF LIGHT	DAY OF THE WEEK							
					SUN	MON	TUE	WED	THUR	FRI	SAT	
0	0715	2	E	ON		X	X	X	X	X		
1	0845	2	E	OFF		X	X	X	X	X		
2	1000	2	E	ON		X	X	X	X	X		
3	1900	2	E	OFF		X	X	X	X	X		
4			E									
5			E									
6			E									
7			E									
8			E									
9			E									
A			E									
B			E									
C			E									
D			E									
E			E									
F			E									

CONTROL CODE "9"													
TIME OF DAY SELECTION FOR COORDINATED CONTROL PLANS													
KEY STROKES 9 + EVENT # + HOUR + MIN + Control Plan + Offset + "E" + DOW LTS													
DATE	BY	#	TIME	CONTROL PLAN	OFFSET	DMPRESS	DAY OF THE WEEK						
							SUN	MON	TUE	WED	THUR	FRI	SAT
		0	0700	7	A	E		X	X	X	X		
		1	0930	E	A	E		X	X	X	X		
		2	1100	8	A	E		X	X	X	X		
		3	1500	9	A	E		X	X	X	X		
		4	1915	E	A	E		X	X	X	X		
		5	1030	3	A	E						X	
		6	1200	4	A	E						X	
		7	1700	3	A	E						X	
		8	1800	E	A	E						X	
		9	1100	3	A	E	X						
		A	1600	E	A	E	X						
		B				E							
		C				E							
		D				E							
		E				E							
		F				E							

"7" KEY ACTIVITY CODE

- 1=TYPE OF SIMULTANEOUS PHASE TERMINATION
- 2=MAX 2 FAZES
- 3=MAX 3 FAZES
- 4=CONDITIONAL SERVICE (1ST SELECT) FAZES SET AT E-F-0
- 5=CONDITIONAL SERVICE (2ND SELECT) FAZES SET AT E-F-1
- 6=ENERGIZE AUX 6 RED
- 7=ENERGIZE AUX 6 GREEN
- 8=ENERGIZE AUX 6 YELLOW
- 9=CONSTANT CALL ON FAZES SET AT D-F-A
- A=TRAFFIC ACTUATED MAX 2 OPERATION
- B=CONSTANT CALL ON FAZES SET AT D-F-B
- C=YELLOW YIELD COORDINATION
- D=YELLOW YIELD COORDINATION
- E=COORD FREE IF F-D-4 = 0
- F=FLASHING OPERATION

SM \_\_\_\_\_ 280 \_\_\_\_\_ 0 \_\_\_\_\_ PM  
 County \_\_\_\_\_ Route \_\_\_\_\_ Location \_\_\_\_\_ Daly City \_\_\_\_\_  
 City \_\_\_\_\_

**INTERSECTION: John Daly & Sheffield**

Group Assignment: NONE

Field Master Assignment: NONE

System Reference Number: 8

N/S Street Name: Not Assigned

E/W Street Name: Not Assigned

Change Record			
Change	By	Date	Change
new Walk and FDW	KH	04/13	
new timing plans	KH	04/13	
<i>new gap mch</i>		<i>12/15</i>	
<i>new yellow time</i>		<i>12/15</i>	

**Notes:**

- Manual Plan
- 0 = Automatic
- 1-9 = Plan 1-9
- 14 = Free
- 15 = Flash

- Manual Offset
- 0 = Automatic
- 1 = Offset A
- 2 = Offset B
- 3 = Offset C

Drop Number	5	<C/0+0+0>
Zone Number	0	<C/0+0+1>
Area Number	0	<C/0+0+2>
Area Address	5	<C/0+0+3>

QuickNet Channel COM101: (QuickNet)

**Communication Addresses**

Manual Plan	
Manual Offset	

**Manual Selection**

Flash Start	8	<F/1+0+E>
Red Revert	5.0	<F/1+0+F>
All Red Start	5.0	<F/1+0+2>

<C/0+A+1>	
<C/0+B+1>	

Exclusive Walk	0	<F/1+0+0>
Exclusive FDW	0	<F/1+0+1>
All Red Clear	0.0	<F/1+0+2>

**Exclusive Ped Phase**

(Outputs specified in Assignable Outputs at E/127+A+E & F)

Row	Phase							
	1	2	3	4	5	6	7	8
Ped Walk	0	5	0	5	0	5	0	0
Ped FDW	0	22	0	26	0	18	0	0
Min Green	3	6	4	5	3	6	0	0
Type 3 Disconnect	0	0	0	0	0	0	0	0
Added per Vehicle	2.0	2.0	2.0	2.0	2.0	2.0	0.0	0.0
Veh Extension	3.0	3.5	3.0	3.0	2.0	3.5	0.0	0.0
Max Gap	3.5	4.0	6.0	4.0	2.5	4.0	0.0	0.0
Min Gap	2.5	3.0	4.0	2.5	1.5	3.0	0.0	0.0
Max Limit	30	45	20	20	10	40	0	0
Max Limit 2	40	50	35	35	15	60	0	0
Adv. / Delay Walk	0	0	0	0	0	0	0	0
PE Min Ped FDW	7	7	7	7	7	7	0	0
Cond Serv Check	10	10	10	10	10	10	0	0
Reduce Every	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Yellow Change	3.0	<del>3.0</del> 3.5	3.0	3.0	3.0	<del>3.0</del> 3.0	0.0	0.0
Red Clear	1.0	1.0	1.5	1.0	0.5	1.0	0.0	0.0

**Phase Timing - Bank 1**

<C+0+F=1>

Phase	9				A				B				C				D			
	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---			
Phase 1	0	0	0	0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0			
Phase 2	20	0	0	0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0			
Phase 3	0	0	0	0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0			
Phase 4	0	0	0	0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0			
Phase 5	0	0	0	0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0			
Phase 6	20	0	0	0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0			
Phase 7	0	0	0	0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0			
Phase 8	20	0	0	0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0			
Max Initial																				
Alternate Walk																				
Alternate FDW																				
Alternate Initial																				
Alternate Extension																				

**Alternate Timing**

<C+0+F=1>

Phase	E			
	RR-1 Delay	RR-1 Clear	EV-A Delay	EV-A Clear
RR-1 Delay	0	0	0	0
RR-1 Clear	0	0	0	0
EV-A Delay	0	0	0	0
EV-A Clear	0	0	0	0
EV-B Delay	0	0	0	0
EV-B Clear	0	0	0	0
EV-C Delay	0	0	0	0
EV-C Clear	0	0	0	0
EV-D Delay	0	0	0	0
EV-D Clear	0	0	0	0
RR-2 Delay	0	0	0	0
RR-2 Clear	0	0	0	0
View EV Delay	---	---	---	---
View EV Clear	---	---	---	---
View RR Delay	---	---	---	---
View RR Clear	---	---	---	---

**Preempt Timing**

Phase	F			
	Permit	Red Lock	Yellow Lock	Min Recall
Permit	0	0	0	0
Red Lock	0	0	0	0
Yellow Lock	0	0	0	0
Min Recall	0	0	0	0
Ped Recall	0	0	0	0
View Set Peds	---	---	---	---
Rest In Walk	0	0	0	0
Red Rest	0	0	0	0
Dual Entry	0	0	0	0
Max Recall	0	0	0	0
Soft Recall	0	0	0	0
Max 2	2	6	6	6
Cond. Service	0	0	0	0
Man Cntrl Calls	0	0	0	0
Yellow Start	0	0	0	0
First Phases	2	6	6	6

**Phase Functions**

<C+0+F=1>

Row	0	1	2	3	4	5	6	7	8
Overlap Name									
Load Switch Number	13	9	0	0	0	0	0	0	0
Veh Set 1 - Phases	34	34							
Veh Set 2 - Phases									
Veh Set 3 - Phases									
Neg Veh Phases	12	56							
Neg Ped Phases									
Green Omit Phases									
Green Clear Omit Phs.									
Green Clear	3.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Yellow Change	3.5	3.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Red Clear	1.5	1.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Overlap Assignments <C+0+E=29>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
EV-A	125															
EV-B	0															
EV-C	0															
EV-D	0															
RR-1 *	---															
RR-2 *	---															
SE-1	0															
SE-2	0															

- Extra 1 Flags  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = Solid FDW on EV  
 5 = Extended Status  
 6 = International Ped  
 7 = Flash - Clear Outputs  
 8 = Split Ring

- Extra 2 Flags  
 1 = AWB During Initial  
 2 = LMU Installed  
 3 = Disable Min Walk  
 4 = QuickNet/4 System  
 5 = Ignore P/P on EV  
 6 =  
 7 = Reserved  
 8 =

Preempt Priority  
 <C+0+E=125>  
 and RR-1 is always Highest,  
 and RR-2 is always  
 Second Highest)

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Exclusive Phases																
RR-1 Clear Phases																
RR-2 Clear Phases																
RR-2 Limited Service																
Prot / Perm Phases																
Flash to PE Circuits																
Flash Entry Phases																
Disable Yellow Range																
Disable Ovp Yel Range																
Overlap Yellow Flash																
EV-A Phases																
EV-B Phases																
EV-C Phases																
EV-D Phases																
Extra 1 Config. Bits	1	3	5													
IC Select (Interconnect)	2															

Configuration <C+0+E=125>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Ext. Permit 1 Phases																
Ext. Permit 2 Phases																
Exclusive Ped Assign																
Preempt Non-Lock	12345678															
Ped for 2P Output	2															
Ped for 6P Output						6										
Ped for 4P Output							4									
Ped for 8P Output								8								
Yellow Flash Phases																
Low Priority A Phases																
Low Priority B Phases																
Low Priority C Phases																
Low Priority D Phases																
Restricted Phases																
Extra 2 Config. Bits																

Configuration <C+0+E=125>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Fast Green Flash Phase																
Green Flash Phases																
Flashing Walk Phases																
Guaranteed Passage																
Simultaneous Gap Term	12345678															
Sequential Timing																
Advance Walk Phases																
Delay Walk Phases																
External Recall																
Start-up Overlap Green																
Max Extension																
Inhibit Ped Reserve/oc																
Semi-Actuated																
Start-up Overlap Yellow																
Start-up Vehicle Calls	12345678															
Start-up Ped Calls	12345678															

Specials <C+0+F=2>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Phase 1	15															
Phase 2	26															
Phase 3	15															
Phase 4	30															
Phase 5	15															
Phase 6	26															
Phase 7	0															
Phase 8	0															

- Flash to PE &  
 PE Non-Lock  
 1 = EV A 5 = RR 1  
 2 = EV B 6 = RR 2  
 3 = EV C 7 = SE 1  
 4 = EV D 8 = SE 2

- IC Select Flags  
 1 =  
 2 = Modem  
 3 = 7-Wire Slave  
 4 = Flash / Free  
 5 =  
 6 = Simplex Master  
 7 = 7-Wire Master  
 8 = Offset Interrupter

Coordination Transition Minimums  
 <C+0+C=5>

Coord Extra

1 = Programmed WALK Time for Sync Phases  
2 = Always Terminate Sync Phase Peds

Plan Name	Plan								
	1	2	3	4	5	6	7	8	9
Cycle Length	116	116	120	110	114	120	0	0	0
Phase 1 - ForceOff	18	22	79	21	75	76	0	0	0
Phase 2 - ForceOff	0	0	0	0	0	0	0	0	0
Phase 3 - ForceOff	33	39	17	34	17	17	0	0	0
Phase 4 - ForceOff	69	75	53	66	53	53	0	0	0
Phase 5 - ForceOff	84	89	67	80	68	68	0	0	0
Phase 6 - ForceOff	0	0	0	20	0	0	0	0	0
Phase 7 - ForceOff	0	0	0	0	0	0	0	0	0
Phase 8 - ForceOff	0	0	0	0	0	0	0	0	0
Ring Offset	0	0	0	0	0	0	0	0	0
Offset 1	40	42	110	45	60	55	0	0	0
Offset 2	0	0	0	0	0	0	0	0	0
Offset 3	0	0	0	0	0	0	0	0	0
Perm 1 - End	15	15	15	15	15	15	0	0	0
Hold Release	255	255	255	255	255	255	0	0	0
Zone Offset	0	0	0	0	0	0	0	0	0

Coordination - Bank 1

<C+0+C=1>

Plan Name	E
Plan 1 - Sync	2 6
Plan 2 - Sync	2 6
Plan 3 - Sync	2 6
Plan 4 - Sync	2 6
Plan 5 - Sync	2 6
Plan 6 - Sync	2 6
Plan 7 - Sync	
Plan 8 - Sync	
Plan 9 - Sync	
NEMA Sync	
NEMA Hold	
Coord Extra	

Sync Phases <C+0+C=1>

Plan Name	1	2	3	4	5	6	7	8	9
Ped Adjustment	0	0	0	0	0	0	0	0	0
Perm 2 - Start	0	0	0	0	0	0	0	0	0
Perm 2 - End	0	0	0	0	0	0	0	0	0
Perm 3 - Start	0	0	0	0	0	0	0	0	0
Perm 3 - End	0	0	0	0	0	0	0	0	0
Reservice Time	0	0	0	0	0	0	0	0	0
Reservice Phases									
Pretimed Phases									
Max Recall	1								
Perm 1 Veh Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
Perm 1 Ped Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
Perm 2 Veh Phase									
Perm 2 Ped Phase									
Perm 3 Veh Phase									
Perm 3 Ped Phase									

Coordination - Bank 2

<C+0+C=2>

Plan Name	F
Free Lag	2 4 6 8
Plan 1 - Lag	1 4 6 8
Plan 2 - Lag	1 4 6 8
Plan 3 - Lag	2 4 6 8
Plan 4 - Lag	2 4 6 8
Plan 5 - Lag	2 4 6 8
Plan 6 - Lag	2 4 6 8
Plan 7 - Lag	
Plan 8 - Lag	
Plan 9 - Lag	
External Lag	

Lag Phases <C+0+C=1>



Row	Column 9	Column A	Column B	Column C	Column D	Column E	Column F
0	Spec. Funct. 1	0 NOT-3	0 Max 2	0 Prefirmed	0 Set Monday	0 Dial 2 (7-Wire)	0 Sim Term
1	Spec. Funct. 2	0 NOT-4	0 System Det 1	0 Plan 1	0 Ext. Perm 1	0 Dial 3 (7-Wire)	0 EV-A
2	Spec. Funct. 3	0 OR-4 (a)	0 System Det 2	0 Plan 2	0 Ext. Perm 2	0 Offset 1 (7-Wire)	0 EV-B
3	Spec. Funct. 4	0 OR-4 (b)	0 System Det 3	0 Plan 3	0 Reserved	0 Offset 2 (7-Wire)	0 EV-C
4	NAND-3 (a)	0 OR-5 (a)	0 System Det 4	0 Plan 4	0 Set Clock	0 Offset 3 (7-Wire)	0 EV-D
5	NAND-3 (b)	0 OR-5 (b)	0 System Det 5	0 Plan 5	0 Stop Time	0 Free (7-Wire)	0 RR-1
6	NAND-4 (a)	0 OR-6 (a)	0 System Det 6	0 Plan 6	0 Flash Sense	0 Flash (7-Wire)	0 RR-2
7	NAND-4 (b)	0 OR-6 (b)	0 System Det 7	0 Plan 7	0 Manual Enable	0 Excl. Ped Omit	0 Spec. Event 1
8	OR-7 (a)	0 Fig 3 Diamond	0 System Det 8	0 Plan 8	0 Man. Advance	0 NOT-1	0 Spec. Event 2
9	OR-7 (b)	0 Fig 4 Diamond	0 Max Inhibit (nema)	0 Plan 9	0 External Alarm	0 NOT-2	0 External Lag
A	OR-7 (c)	0 AND-4 (a)	0 Force A (nema)	0 DELAY-A	0 Phase Bank 2	0 OR-1 (a)	0 AND-1 (a)
B	OR-7 (d)	0 AND-4 (b)	0 Force B (nema)	0 DELAY-B	0 Phase Bank 3	0 OR-1 (b)	0 AND-1 (b)
C	OR-8 (a)	0 NAND-1 (a)	0 C.N.A. (nema)	0 DELAY-C	0 Overlap Set 2	0 OR-2 (a)	0 AND-2 (a)
D	OR-8 (b)	0 NAND-1 (b)	0 Hold (nema)	0 DELAY-D	0 Overlap Set 3	0 OR-2 (b)	0 AND-2 (b)
E	OR-8 (c)	0 NAND-2 (a)	0 Max Recall	0 DELAY-E	0 Detector Set 2	0 OR-3 (a)	0 AND-3 (a)
F	OR-8 (d)	0 NAND-2 (b)	0 Min Recall	0 DELAY-F	0 Detector Set 3	0 OR-3 (b)	0 AND-3 (b)

Assignable Inputs

<C+0+E=126>

Row	Column 9	Column A	Column B	Column C	Column D	Column E	Column F
0	Phase ON - 1	0 Preempt Fail	0 Flasher 0	0 Free	0 NOI-1	0 TOD Out 1	0 Dial 2 (7-Wire)
1	Phase ON - 2	0 Sp Evt Out 1	0 Flasher 1	0 Plan 1	0 OR-1	0 TOD Out 2	0 Dial 3 (7-Wire)
2	Phase ON - 3	0 Sp Evt Out 2	0 Fast Flasher	0 Plan 2	0 OR-2	0 TOD Out 3	0 Offset 1 (7-Wire)
3	Phase ON - 4	0 Sp Evt Out 3	0 Fig 3 Diamond	0 Plan 3	0 OR-3	0 TOD Out 4	0 Offset 2 (7-Wire)
4	Phase ON - 5	0 Sp Evt Out 4	0 Fig 4 Diamond	0 Plan 4	0 AND-1	0 TOD Out 5	0 Offset 3 (7-Wire)
5	Phase ON - 6	0 Sp Evt Out 5		0 Plan 5	0 AND-2	0 TOD Out 6	0 Free (7-Wire)
6	Phase ON - 7	0 Sp Evt Out 6		0 Plan 6	0 AND-3	0 TOD Out 7	0 Flash (7-Wire)
7	Phase ON - 8	0 Sp Evt Out 7		0 Plan 7	0 NOT-2	0 TOD Out 8	0 Preempt
8	Ph. Check - 1	0 Sp Evt Out 8	0 NOT-3	0 Plan 8	0 EV-A	0 Adv. Warm - 1	0 Low Priority A
9	Ph. Check - 2	0	0 NOT-4	0 Plan 9	0 EV-B	0 Adv. Warm - 2	0 Low Priority B
A	Ph. Check - 3	0 Detector Fail	0 OR-4	0 Spec. Funct. 3	0 EV-C	0 DELAY-A	0 Low Priority C
B	Ph. Check - 4	0 Spec. Funct. 1	0 OR-5	0 Spec. Funct. 4	0 EV-D	0 DELAY-B	0 Low Priority D
C	Ph. Check - 5	0 Spec. Funct. 2	0 OR-6	0 NAND-3	0 RR-1	0 DELAY-C	
D	Ph. Check - 6	0 Central Control	0 AND-4	0 NAND-4	0 RR-2	0 DELAY-D	
E	Ph. Check - 7	0 Excl. Ped DW	0 NAND-1	0 OR-7	0 Spec. Event 1	0 DELAY-E	
F	Ph. Check - 8	0 Excl. Ped WK	0 NAND-2	0 OR-8	0 Spec. Event 2	0 DELAY-F	

Assignable Outputs

<C+0+E=127>



Row	Phase Names	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk	0	7	0	7	0	7	0	7
1	Ped FDW	0	15	0	15	0	15	0	15
2	Min Green	4	7	4	4	4	7	4	4
3	Type 3 Disconnect	0	20	0	20	0	20	0	20
4	Added per Vehicle	0.0	2.0	0.0	2.0	0.0	2.0	0.0	2.0
5	Veh Extension	2.0	4.0	2.0	2.5	2.0	4.0	2.0	2.5
6	Max Gap	3.0	6.0	3.0	3.0	3.0	6.0	3.0	3.0
7	Min Gap	0.5	2.0	0.5	1.5	0.5	2.0	0.5	1.5
8	Max Limit	20	30	20	25	20	30	20	25
9	Max Limit 2	30	50	30	40	30	50	30	40
A	Adv. / Delay Walk	0	0	0	0	0	0	0	0
B	PE Min Ped FDW	7	7	7	7	7	7	7	7
C	Cond Serv Check	10	10	10	10	10	10	10	10
D	Reduce Every	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
E	Yellow Change	3.0	4.0	3.0	3.0	3.0	4.0	3.0	3.0
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

Phase Timing - Bank 2 <C+0+F=2>

Row	Phase Names	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk	0	7	0	7	0	7	0	7
1	Ped FDW	0	15	0	15	0	15	0	15
2	Min Green	4	7	4	4	4	7	4	4
3	Type 3 Disconnect	0	20	0	20	0	20	0	20
4	Added per Vehicle	0.0	2.0	0.0	2.0	0.0	2.0	0.0	2.0
5	Veh Extension	2.0	4.0	2.0	2.5	2.0	4.0	2.0	2.5
6	Max Gap	3.0	6.0	3.0	3.0	3.0	6.0	3.0	3.0
7	Min Gap	0.5	2.0	0.5	1.5	0.5	2.0	0.5	1.5
8	Max Limit	20	30	20	25	20	30	20	25
9	Max Limit 2	30	50	30	40	30	50	30	40
A	Adv. / Delay Walk	0	0	0	0	0	0	0	0
B	PE Min Ped FDW	7	7	7	7	7	7	7	7
C	Cond Serv Check	10	10	10	10	10	10	10	10
D	Reduce Every	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
E	Yellow Change	3.0	4.0	3.0	3.0	3.0	4.0	3.0	3.0
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

Phase Timing - Bank 3 <C+0+F=3>

Phase	9			
	A	B	C	D
Phase 1	0	0	0	0
Phase 2	0	0	0	0
Phase 3	0	0	0	0
Phase 4	0	0	0	0
Phase 5	0	0	0	0
Phase 6	0	0	0	0
Phase 7	0	0	0	0
Phase 8	0	0	0	0

Alternate Timing

Phase	9			
	A	B	C	D
Phase 1	0	0	0	0
Phase 2	0	0	0	0
Phase 3	0	0	0	0
Phase 4	0	0	0	0
Phase 5	0	0	0	0
Phase 6	0	0	0	0
Phase 7	0	0	0	0
Phase 8	0	0	0	0

Alternate Timing

Transition Type  
 0X = Shortway  
 1,X = Lengthen  
 X:1 thru X:4 =  
 Number of  
 cycles when  
 lengthening

Daylight Savings  
 Date  
 If set to all zeros,  
 standard dates  
 will be used.

Transition Type <C/5+1+9>  
**TBC Transition**  
 1.3

Lag Hold Phases <C/5+1+A>  
**Coordinated Lag Hold Phases**

Sync Output Time <C/5+1+C>  
**7-Wire Master**  
 0.0

Begin Month 3 <C/5+2+A>  
 Begin Week 2 <C/5+2+B>  
 End Month 11 <C/5+2+C>  
 End Week 1 <C/5+2+D>  
**Daylight Savings Time**

Time B4 Yellow <F/1+C+E>  
 Phase Number 0 <F/1+C+F>  
**Advance Warning Beacon - Sign 1**

Time B4 Yellow <F/1+D+E>  
 Phase Number 0 <F/1+D+F>  
**Advance Warning Beacon - Sign 2**

Long Failure <F/1+0+6>  
 Short Failure <F/1+0+7>  
**Power Cycle Correction** (Default = 0.7)  
 0.7







Min Time (seconds)  <F/1+0+8>  
**Min Green Before PE Force Off**

Max Time (minutes)  <F/1+0+9>  
**Max Preempt Time Before Failure**

Min Time (seconds)  <F/1+0+A>  
**Min Time Between Same Preempts**  
(Does Not Apply To Railroad Preempt)

Low Pri. Channel  <E/125+C+8>  
**Disable Low Priority Channel**

- Low Priority
- 1 = Channel A
- 2 = Channel B
- 3 = Channel C
- 4 = Channel D

Delay Time (seconds)  <F/1+A+D>  
**Bus Delay**

Max Time (seconds)  <F/1+A+E>  
**Max Early Green**

Max Time (seconds)  <F/1+A+F>  
**Max Green Extension**

Headway Time (minutes)  
 1 thru 9 = 1 thru 9  
 A = 10  
 B = 11  
 C = 12  
 D = 13  
 E = 14  
 F = 15

Row	Time	Headway	Direction	Day of Week
0	00:00	0	0	
1	00:00	0	0	
2	00:00	0	0	
3	00:00	0	0	
4	00:00	0	0	
5	00:00	0	0	
6	00:00	0	0	
7	00:00	0	0	
8	00:00	0	0	
9	00:00	0	0	
A	00:00	0	0	
B	00:00	0	0	
C	00:00	0	0	
D	00:00	0	0	
E	00:00	0	0	
F	00:00	0	0	

Headway <C+0+9=2.1>

**Low Priority Preemption (Bus Priority)**

Only available with Program 233RV2.B (and above)  
 Note: Also see "Time of Day Functions", Function E, Bit 5 (Disable Low Priority)



Change Record			
Change	By	Date	Change
Update Timing	Iteris	08/09	
Fine-Tuned	Iteris	09/09	

Notes: Proposed red light camera for South approach  
 No right turn on red: EB, WB

Manual Plan  
 0 = Automatic  
 1-9 = Plan 1-9  
 14 = Free  
 15 = Flash  
 Manual Offset  
 0 = Automatic  
 1 = Offset A  
 2 = Offset B  
 3 = Offset C

Drop Number	1	<C/0+0+0>
Zone Number	1	<C/0+0+1>
Area Number	1	<C/0+0+2>
Area Address	2	<C/0+0+3>
QuickNet Channel	comt:	(QuickNet)

Manual Plan	
Manual Offset	

**Manual Selection**

Flash Start	10	<F/1+0+E>
Red Revert	2.0	<F/1+0+F>
All Red Start	5.0	<F/1+0+2>

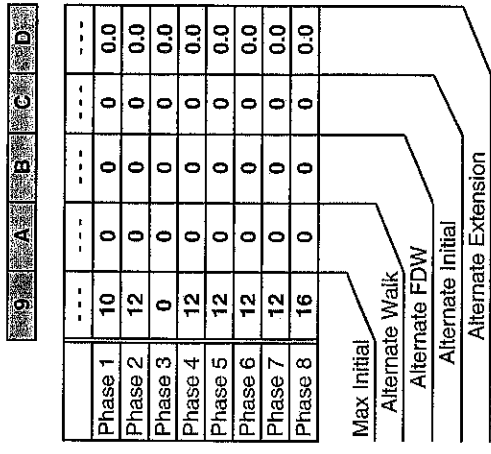
**Start / Revert Times**

Exclusive Walk	0	<F/1+0+0>
Exclusive FDW	0	<F/1+0+1>
All Red Clear	0.0	<F/1+0+2>

**Exclusive Ped Phase**  
 (Outputs specified in Assignable  
 Outputs at E/127+A+E & F)

Phase	Phase							
	1	2	3	4	5	6	7	8
Ped Walk	SBL	NBT	EB1	NBL	SBT	EB2	WB	
Ped FDW			5	25	23			
Min Green	4	8	6	6	8	6	10	
Type 3 Disconnect	2.0	2.0	2.0	2.0	2.0	2.0	2.0	
Added per Vehicle	1.0	1.0	1.0	1.0	1.0	1.0	1.0	
Veh Extension	2.0	3.5	2.0	2.0	3.5	2.0	2.0	
Max Gap	1.0	2.0	1.0	1.0	2.0	1.0	1.0	
Min Limit	30	55	35	30	60	30	25	
Max Limit 2	40	40	40	40	40	40	40	
Adv. / Delay Walk								
RR Min Ped FDW								
Cond Serv Check	1.0	1.0	1.0	1.0	1.0	1.0	1.0	
Reduce Every	3.0	3.6	3.0	3.0	3.6	3.0	3.0	
Yellow Change	1.0	1.0	1.0	1.0	1.0	1.0	1.0	
Red Clear								

**Phase Timing - Bank 1**  
 F + phase + interval <C+0+F=1>



**Alternate Timing** <C+0+F=1>

9	A	B	C	D
RR-1 Delay	0	0	0	0.0
RR-1 Clear	0	0	0	0.0
EV-A Delay	3	3	3	3
EV-A Clear	3	3	3	3
EV-B Delay	3	3	3	3
EV-B Clear	3	3	3	3
EV-C Delay	3	3	3	3
EV-C Clear	3	3	3	3
EV-D Delay	3	3	3	3
EV-D Clear	3	3	3	3
RR-2 Delay	0	0	0	0.0
RR-2 Clear	0	0	0	0.0
View EV Delay	---	---	---	---
View EV Clear	---	---	---	---
View RR Delay	---	---	---	---
View RR Clear	---	---	---	---

**Preempt Timing**

Row	12	45678
Permit		
Red Lock		
Yellow Lock		
Min Recall		
Ped Recall		
View Set Peds		
Rest In Walk		
Red Rest		
Dual Entry		4
Max Recall		
Soft Recall		2 6
Max 2		
Cond. Service		
Man Cntrl Calls		
Yellow Start		
First Phases		2 6

**Phase Functions** <C+0+F=1>  
 F + F + interval



Row	Overlap							
	1	2	3	4	5	6	7	8
0	Column Numbers ---->							
1	Overlap Name ---->							
2	0	0	0	0	0	0	0	0
3								
4								
5								
6								
7								
8								
9								
A								
B								
C								
D	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
E	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
F	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Overlap Assignments <C+0+E=29>

Row	C
0	0
1	0
2	0
3	0
4	---
5	---
6	0
7	0

- Extra 1 Flags  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Extended Status  
 6 = International Ped  
 7 = Flash - Clear Outputs  
 8 = Split Ring

- Extra 2 Flags  
 1 = AWB During Initial  
 2 = LHM Installed  
 3 = Disable Min Walk  
 4 = QuickNet/4 System  
 5 = Ignore P/P on EV  
 6 =  
 7 = Reserved  
 8 =

Preempt Priority  
 <C+0+E=125>  
 (\* RR-1 is always Highest, and RR-2 is second Highest)

Row	E
0	
1	
2	
3	
4	
5	
6	
7	
8	
9	
A	
B	
C	
D	
E	1 3 5
F	2

Configuration <C+0+E=125>  
 E + E + interval

Row	F
0	
1	
2	
3	
4	
5	
6	
7	
8	
9	
A	
B	
C	
D	
E	
F	

Configuration <C+0+E=125>  
 E + F + interval

Row	F
0	
1	
2	
3	
4	
5	
6	
7	
8	
9	
A	
B	
C	
D	
E	
F	

Specials <C+0+F=2>  
 F + F + interval

Row	2
0	
1	10
2	10
3	10
4	10
5	10
6	10
7	10
8	10
9	
A	
B	
C	
D	
E	
F	

Coordination Transition Minimums <C+0+C=5>

- Flash to PE & PE Non-Lock  
 1 = EV A 5 = RR 1  
 2 = EV B 6 = RR 2  
 3 = EV C 7 = SE 1  
 4 = EV D 8 = SE 2
- IC Select Flags  
 1 =  
 2 = Modern  
 3 = 7-Wire Slave  
 4 = Flash / Free  
 5 =  
 6 = Simplex Master  
 7 = 7-Wire Master  
 8 = Offset Interrupter

6/10/2014

INTERSECTION: JUNIPERO SERRA BOULEVARD / WASHINGTON STREET

SHEET

3 OF

8

(Coord Extra Bit 1 = Programmed WALK Time for Sync Phases)

Column Numbers	1	2	3	4	5	6	7	8	9
Cycle Length	120	110	110	110	100	120	100	100	100
Phase 1 - ForceOff	86	21 69	92	78 71	56	25	53	53	53
Phase 2 - ForceOff	16	54 0	17 26	0	0	0	0	0	0
Phase 3 - ForceOff	16	0	0	0	0	25	0	0	0
Phase 4 - ForceOff	61	0 40	73	56 46	36	69	43	43	43
Phase 5 - ForceOff	16	14 69	18 26	71	56	93	58	58	58
Phase 6 - ForceOff	0	0 0	0	0	0	25	0	0	0
Phase 7 - ForceOff	38	0 25	45 53	35 28	18	47	20	20	20
Phase 8 - ForceOff	61	0 40	73	56 46	36	69	43	43	43
Ring Offset									
Offset 1	100	32	40	106	24	102	52	42	33
Offset 2									
Offset 3									
Perm 1 - End	15	15	15	15	15	15	15	15	15
Hold Release	255	255	255	255	255	255	255	255	255
Zone Offset									

<C+0+C=1>

Coordination - Bank 1

Plan	E
Plan 1 - Sync	6
Plan 2 - Sync	2 4 6 8
Plan 3 - Sync	2 6
Plan 4 - Sync	2 6
Plan 5 - Sync	2 6
Plan 6 - Sync	6
Plan 7 - Sync	2 6
Plan 8 - Sync	2 6
Plan 9 - Sync	2 6
NEMA Sync	
NEMA Hold	
Coord Extra	

Sync Phases <C+0+C=1>

Row	0	1	2	3	4	5	6	7	8	9
Ped Adjustment	0	0	0	0	0	0	0	0	0	0
Perm 2 - Start	0	0	0	0	0	0	0	0	0	0
Perm 2 - End	0	0	0	0	0	0	0	0	0	0
Perm 3 - Start	0	0	0	0	0	0	0	0	0	0
Perm 3 - End	0	0	0	0	0	0	0	0	0	0
Reservice Time	0	0	0	0	0	0	0	0	0	0
Reservice Phases										
Pretimed Phases										
Max Recall										
Perm 1 Veh Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
Perm 1 Ped Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
Perm 2 Veh Phase										
Perm 2 Ped Phase										
Perm 3 Veh Phase										
Perm 3 Ped Phase										

<C+0+C=2>

Coordination - Bank 2

Row	0	1	2	3	4	5	6	7	8	9
Free Lag	2 45 8									
Plan 1 - Lag	2 45 8									
Plan 2 - Lag	2 4 6 8									
Plan 3 - Lag	2 45 8									
Plan 4 - Lag	2 4 6 8									
Plan 5 - Lag	2 4 6 8									
Plan 6 - Lag	1 4 6 8									
Plan 7 - Lag	2 4 6 8									
Plan 8 - Lag	2 4 6 8									
Plan 9 - Lag	2 4 6 8									
External Lag										

Lag Phases <C+0+C=1>

Row	Column A	Column B	Column C	Column D	Column E	Column F						
0	Spec. Funct. 1	0	NOT-3	0	Prelimed	0	Set Monday	0	Dial 2 (7-Wire)	0	Sim Term	0
1	Spec. Funct. 2	0	NOT-4	0	Plan 1	0	Ext. Perm 1	0	Dial 3 (7-Wire)	0	EV-A	71
2	Spec. Funct. 3	0	OR-4 (a)	0	Plan 2	0	Ext. Perm 2	0	Offset 1 (7-Wire)	0	EV-B	72
3	Spec. Funct. 4	0	OR-4 (b)	0	Plan 3	0	Dimming	0	Offset 2 (7-Wire)	0	EV-C	73
4	NAND-3 (a)	0	OR-5 (a)	0	Plan 4	0	Set Clock	0	Offset 3 (7-Wire)	0	EV-D	74
5	NAND-3 (b)	0	OR-5 (b)	0	Plan 5	0	Stop Time	82	Free (7-Wire)	0	RR-1	51
6	NAND-4 (a)	0	OR-6 (a)	0	Plan 6	0	Flash Sense	81	Flash (7-Wire)	0	RR-2	52
7	NAND-4 (b)	0	OR-6 (b)	0	Plan 7	0	Manual Enable	0	Excl. Ped Omit	0	Spec. Event 1	0
8	OR-7 (a)	0	Fig 3 Diamond	0	Plan 8	0	Man. Advance	0	NOT-1	0	Spec. Event 2	0
9	OR-7 (b)	0	Fig 4 Diamond	0	Plan 9	0	External Alarm	0	NOT-2	0	External Lag	0
A	OR-7 (c)	0	AND-4 (a)	0	DELAY-A	0	Phase Bank 2	0	OR-1 (a)	0	AND-1 (a)	0
B	OR-7 (d)	0	AND-4 (b)	0	DELAY-B	0	Phase Bank 3	0	OR-1 (b)	0	AND-1 (b)	0
C	OR-8 (a)	0	NAND-1 (a)	0	DELAY-C	0	Overlap Set 2	0	OR-2 (a)	0	AND-2 (a)	0
D	OR-8 (b)	0	NAND-1 (b)	0	DELAY-D	0	Overlap Set 3	0	OR-2 (b)	0	AND-2 (b)	0
E	OR-8 (c)	0	NAND-2 (a)	0	DELAY-E	0	Detector Set 2	0	OR-3 (a)	0	AND-3 (a)	0
F	OR-8 (d)	0	NAND-2 (b)	0	DELAY-F	0	Detector Set 3	0	OR-3 (b)	0	AND-3 (b)	0

Assignable Inputs

<C=0+E=126>

Row	Column A	Column B	Column C	Column D	Column E	Column F						
0	Phase ON - 1	0	Preempt Fail	0	Free	0	NOT-1	0	TOD Out 1	0	Dial 2 (7-Wire)	0
1	Phase ON - 2	0	Sp Evt Out 1	0	Flasher 0	0	Flasher 1	0	OR-1	0	Dial 3 (7-Wire)	0
2	Phase ON - 3	0	Sp Evt Out 2	0	Flasher 1	0	Fast Flasher	0	OR-2	0	Offset 1 (7-Wire)	0
3	Phase ON - 4	0	Sp Evt Out 3	0	Fig 3 Diamond	0	Fig 3 Diamond	0	OR-3	0	Offset 2 (7-Wire)	0
4	Phase ON - 5	0	Sp Evt Out 4	0	Fig 4 Diamond	0	Fig 4 Diamond	0	AND-1	0	Offset 3 (7-Wire)	0
5	Phase ON - 6	0	Sp Evt Out 5	0		0		0	AND-2	0	Free (7-Wire)	0
6	Phase ON - 7	0	Sp Evt Out 6	0		0		0	AND-3	0	Flash (7-Wire)	0
7	Phase ON - 8	0	Sp Evt Out 7	0		0		0	NOT-2	0	Preempt	0
8	Ph. Check - 1	0	Sp Evt Out 8	0	NOT-3	0	NOT-4	0	EV-A	0	Low Priority A	0
9	Ph. Check - 2	0	Detector Fail	0	NOT-4	0		0	EV-B	0	Low Priority B	0
A	Ph. Check - 3	0	Spec. Funct. 1	0	OR-4	0	Spec. Funct. 3	0	EV-C	0	Low Priority C	0
B	Ph. Check - 4	0	Spec. Funct. 2	0	OR-5	0	Spec. Funct. 4	0	EV-D	0	Low Priority D	0
C	Ph. Check - 5	0	Spec. Funct. 3	0	OR-6	0	NAND-3	0	RR-1	0		
D	Ph. Check - 6	0	Central Control	0	AND-4	0	NAND-4	0	RR-2	0		
E	Ph. Check - 7	0	Excl. Ped DW	0	NAND-1	0	OR-7	0	Spec. Event 1	0		
F	Ph. Check - 8	0	Excl. Ped WK	0	NAND-2	0	OR-8	0	Spec. Event 2	0		

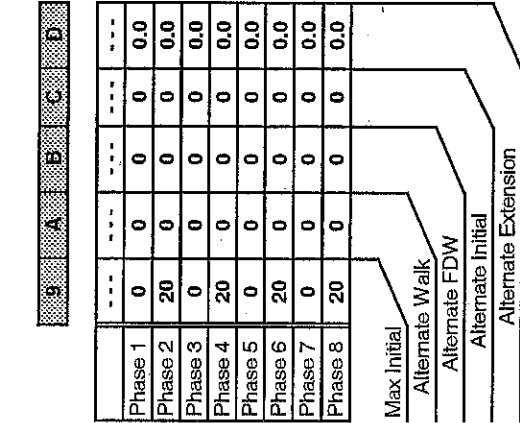
Assignable Outputs

<C=0+E=127>

Row	Phase Names ---->	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk	0	7	0	7	0	7	0	7
1	Ped FDW	0	15	0	15	0	15	0	15
2	Min Green	4	7	4	4	4	7	4	4
3	Type 3 Disconnect	0	20	0	20	0	20	0	20
4	Added per Vehicle	0.0	2.0	0.0	2.0	0.0	2.0	0.0	2.0
5	Veh Extension	2.0	4.0	2.0	2.5	2.0	4.0	2.0	2.5
6	Max Gap	3.0	6.0	3.0	3.0	3.0	6.0	3.0	3.0
7	Min Gap	0.5	2.0	0.5	1.5	0.5	2.0	0.5	1.5
8	Max Limit	20	30	20	25	20	30	20	25
9	Max Limit 2	30	50	30	40	30	50	30	40
A	Adv. / Delay Walk	0	0	0	0	0	0	0	0
B	RR Min Ped FDW	7	7	7	7	7	7	7	7
C	Cond Serv Check	10	10	10	10	10	10	10	10
D	Reduce Every	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
E	Yellow Change	3.0	4.0	3.0	3.0	3.0	4.0	3.0	3.0
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

Phase Timing - Bank 2

<C=0+F=2>

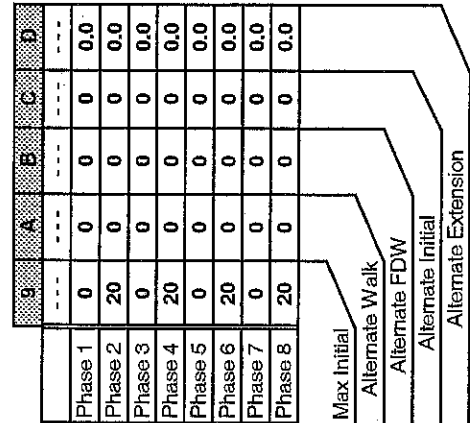


Alternate Timing

Row	Phase Names ---->	Phase							
		1	2	3	4	5	6	7	8
0	Ped Walk	0	7	0	7	0	7	0	7
1	Ped FDW	0	15	0	15	0	15	0	15
2	Min Green	4	7	4	4	4	7	4	4
3	Type 3 Disconnect	0	20	0	20	0	20	0	20
4	Added per Vehicle	0.0	2.0	0.0	2.0	0.0	2.0	0.0	2.0
5	Veh Extension	2.0	4.0	2.0	2.5	2.0	4.0	2.0	2.5
6	Max Gap	3.0	6.0	3.0	3.0	3.0	6.0	3.0	3.0
7	Min Gap	0.5	2.0	0.5	1.5	0.5	2.0	0.5	1.5
8	Max Limit	20	30	20	25	20	30	20	25
9	Max Limit 2	30	50	30	40	30	50	30	40
A	Adv. / Delay Walk	0	0	0	0	0	0	0	0
B	RR Min Ped FDW	7	7	7	7	7	7	7	7
C	Cond Serv Check	10	10	10	10	10	10	10	10
D	Reduce Every	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
E	Yellow Change	3.0	4.0	3.0	3.0	3.0	4.0	3.0	3.0
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

Phase Timing - Bank 3

<C=0+F=3>



Alternate Timing

Transition Type **0.3** <C/5+1+9>

**TBC Transition**

Lag Hold Phases **0.0** <C/5+1+A>

**Coordinated Lag Hold Phases**

Sync Output Time **0.0** <C/5+1+C>

**7-Wire Master**

Begin Month **4** <C/5+2+A>

Begin Week **1** <C/5+2+B>

End Month **10** <C/5+2+C>

End Week **5** <C/5+2+D>

**Daylight Savings Time**

Time Before Yellow **0.0** <F/1+C+E>

Phase Number **0** <F/1+C+F>

**Advance Warning Beacon - Sign 1**

Time Before Yellow **0.0** <F/1+D+E>

Phase Number **0** <F/1+D+F>

**Advance Warning Beacon - Sign 2**

Long Failure **0.7** <F/1+0+6>

Short Failure **0.7** <F/1+0+7>

**Power Cycle Correction** (Default = 0.5)

Min Time (seconds) **0** <F/1+0+8>

**Min Green Before PE Force Off**

Max Time (minutes) **255** <F/1+0+9>

**Max Preempt Time Before Failure**

Min Time (seconds) **0** <F/1+0+A>

**Min Time Between Same Preempts**

(Does Not Apply To Railroad Preempt)

Low Pri. Channel **0** <E/125+C+8>

**Disable Low Priority Channel**

Transition Type

0.X = Shortway

1.X = Lengthen

X.1 thru X.4 =

Number of

cycles when

lengthening

Low Priority

1 = Channel A

2 = Channel B

3 = Channel C

4 = Channel D

Column Numbers -->

Row	Detector Name	Input Slot	Det #	1	2	3	1	3
1	2	3	4	5	6	7	8	9
1	2/2U	1	39	45 7	2	123 8	0.0	0.0
2	6/2U	2	40	45 7	6	123 8	0.0	0.0
3	4/6U	3	41	45 7	4	123 8	0.0	0.0
4	8/6U	4	42	45 7	8	123 8	0.0	0.0
5	2/2L	5	43	45 7	2	123 8	0.0	0.0
6	6/2L	6	44	45 7	6	123 8	0.0	0.0
7	4/6L	7	45	45 7	4	123 8	0.0	0.0
8	8/6L	8	46	45 7	8	123 8	0.0	0.0
9	2/4	9	47	67	2	123 8	0.0	0.0
10	6/4	10	48	67	6	123 8	0.0	0.0
11	4/8	11	49	67	4	123 8	0.0	0.0
12	8/8	12	50	67	8	123 8	0.0	0.0
13	5/1	13	55	45 7	5	123 8	0.0	0.0
14	1/1	14	56	45 7	1	123 8	0.0	0.0
15	7/5	15	57	45 7	7	123 8	0.0	0.0
16	3/5	16	58	45 7	3	123 8	0.0	0.0

Row	Column Numbers -->	1	2	3	4	5	6	7
1	Walk	0	0	0	0	0	0	0
2	Don't Walk	0	0	0	0	0	0	0
3	Phase Green	0	0	0	0	0	0	0
4	Phase Yellow	0	0	0	0	0	0	0
5	Phase Red	0	0	0	0	0	0	0
6	Overlap Green	0	0	0	0	0	0	0
7	Overlap Yellow	0	0	0	0	0	0	0
8	Overlap Red	0	0	0	0	0	0	0

**Redirect Phase Outputs** <C+0+E=127>

Cabinet Type 0 <E/125+D+0>

**Enable Redirection**

(Enable Redirection = 30)

Max OFF (minutes) 20 <D/0+0+1>

Max ON (minutes) 7 <D/0+0+2>

**Detector Failure Monitor**

Row	Detector Name	Input Slot	Det #	4	5	6	7	2	4
1	2	3	4	5	6	7	8	9	10
17	5/9U	17	59	45 7	5	123 8	0.0	0.0	
18	1/9U	18	60	45 7	1	123 8	0.0	0.0	
19	7/9L	19	61	45 7	7	123 8	0.0	0.0	
20	3/9L	20	62	45 7	3	123 8	0.0	0.0	
21	2/9U	21	63	45 7	2	123 8	0.0	0.0	
22	6/9U	22	64	45 7	6	123 8	0.0	0.0	
23	4/7U	23	65	45 7	4	123 8	0.0	0.0	
24	8/7U	24	66	45 7	8	123 8	0.0	0.0	
25		25	67	2	2	123 8	0.0	0.0	
26		26	68	2	6	123 8	0.0	0.0	
27		27	69	2	4	123 8	0.0	0.0	
28		28	70	2	8	123 8	0.0	0.0	
29	2/9L	29	76	45 7	2	123 8	0.0	0.0	
30	6/9L	30	77	45 7	6	123 8	0.0	0.0	
31	4/7L	31	78	45 7	4	123 8	0.0	0.0	
32	8/7L	32	79	45 7	8	123 8	0.0	0.0	

**Detector Assignments**

<C+0+E=126>

**Detector Attributes**

1 = Full Time Delay

2 = Ped Call

3 =

4 = Court

5 = Extension

6 = Type 3

7 = Calling

8 = Alternate

**Det. Assignments**

1 = Det. Set 1

2 = Det. Set 2

3 = Det. Set 3

4 =

5 =

6 = Failure - Min

Recall

7 = Failure - Max

Recall

8 = Report on Failure

Row	1	2	3	4	5	6	7
1	Output Port 1						
2	Output Port 2						
3	Output Port 3						
4	Output Port 4						
5	Output Port 5						
6	Output Port 6						
7	Output Port 7						

**Dimming** <C+0+E=125>

Row	A	B	C	D	E	F
1	0	0	0	0	0	0
2	0	0	0	0	0	0
3	0	0	0	0	0	0
4	0	0	0	0	0	0
5	0	0	0	0	0	0
6	0	0	0	0	0	0
7	0	0	0	0	0	0

**Disable Alarms**

1 = Stop Time

2 = Flash Sense

3 = Keyboard Entry

4 = Manual Plan

5 = Police Control

6 = External Alarm

7 = Detector Failure

8 =

**Delay Logic Times**

<C+0+D=0> (seconds)

Omit Alarm <C/5+F+0>

**Disable Alarm Reporting**

Time 10 <C/5+C+0>

**Redial Time (minutes)**

Dial-Back Telephone Number <C+0+C=5>



Row	6	7	8	9	A	B	C	D	E	F
	Clear	Time	Ped Call	Hold	Advance	Force Off	Vehicle Call	Permit Phases	Ped Omnit	Output
0		0								
1		0								
2		0								
3		0								
4		0								
5		0								
6		0								
7		0								
8		0								
9		0								
A		0								
B		0								
C		0								
D		0								
E		0								
F		0								

<C+0+E=27>

Special Event Schedule -- Table 1

Notes:

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

0 <E/27+5+F>

Limited Service Interval

Row	6	7	8	9	A	B	C	D	E	F
	Clear	Time	Ped Call	Hold	Advance	Force Off	Vehicle Call	Permit Phases	Ped Omnit	Output
0		0								
1		0								
2		0								
3		0								
4		0								
5		0								
6		0								
7		0								
8		0								
9		0								
A		0								
B		0								
C		0								
D		0								
E		0								
F		0								

<C+0+E=28>

Special Event Schedule -- Table 2

Notes:

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

0 <E/28+5+F>

Limited Service Interval



Change Record			
Change	By	Date	Change
Update Timing	Iteris	08/09	
Fine-Tuned	Iteris	09/09	

Drop Number	3	<C/0+0+0>
Zone Number	1	<C/0+0+1>
Area Number	1	<C/0+0+2>
Area Address	3	<C/0+0+3>
QuickNet Channel	com1:	(QuickNet)

**Manual Selection**

Manual Plan	
Manual Offset	

Notes: Red light camera for East approach  
 Master location

- Manual Plan  
 0 = Automatic  
 1-9 = Plan 1-9  
 14 = Free  
 15 = Flash  
 Manual Offset  
 0 = Automatic  
 1 = Offset A  
 2 = Offset B  
 3 = Offset C

Flash Start	10	<F/1+0+E>
Red Revert	2.0	<F/1+0+F>
All Red Start	5.0	<F/1+C+0>

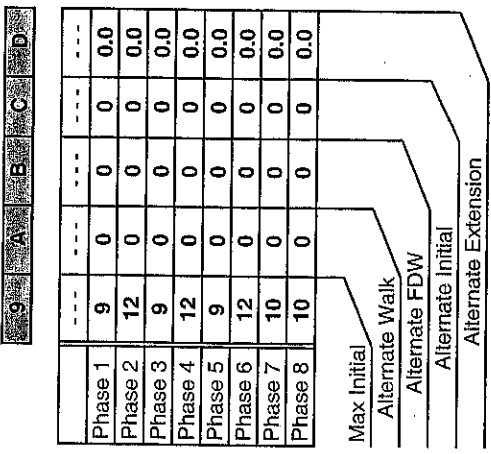
**Start / Revert Times**

Exclusive Walk	0	<F/1+0+0>
Exclusive FDW	0	<F/1+0+1>
All Red Clear	0.0	<F/1+0+2>

**Exclusive Ped Phase**  
 (Outputs specified in Assignable  
 Outputs at E/127+A+E & F)

Phase Names ---->	Phase							
	1	2	3	4	5	6	7	8
Ped Walk								
Ped FDW								
Min Green	3	8	3	6	3	8	4	6
Type 3 Disconnect								
Added per Vehicle	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
Veh Extension	1.0	3.0	5.0	2.0	3.0	3.0	3.0	2.0
Max Gap	2.0	3.0	5.0	2.0	3.0	2.0	3.0	2.0
Min Gap	1.0	2.0	3.0	1.0	2.0	2.0	1.0	2.0
Max Limit	20	30	40	30	30	20	30	20
Max Limit 2	40	40	40	40	40	40	40	40
Adv. / Delay Walk								
RR Min Ped FDW								
Cond Serv Check								
Reduce Every								
Yellow Change	3.2	3.6	3.2	3.2	3.2	3.6	3.2	3.2
Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

**Phase Timing - Bank 1**  
 F + phase + interval <C+0+F=1>



**Alternate Timing** <C+0+F=1>

Phase	RR-1 Delay	RR-1 Clear	EV-A Delay	EV-A Clear	EV-B Delay	EV-B Clear	EV-C Delay	EV-C Clear	EV-D Delay	EV-D Clear	RR-2 Delay	RR-2 Clear	View EV Delay	View EV Clear	View RR Delay	View RR Clear
Phase 1	9	0	0	0	0	0	0	0	0	0	0	0	---	---	---	---
Phase 2	12	0	0	0	0	0	0	0	0	0	0	0	---	---	---	---
Phase 3	9	0	0	0	0	0	0	0	0	0	0	0	---	---	---	---
Phase 4	12	0	0	0	0	0	0	0	0	0	0	0	---	---	---	---
Phase 5	9	0	0	0	0	0	0	0	0	0	0	0	---	---	---	---
Phase 6	12	0	0	0	0	0	0	0	0	0	0	0	---	---	---	---
Phase 7	10	0	0	0	0	0	0	0	0	0	0	0	---	---	---	---
Phase 8	10	0	0	0	0	0	0	0	0	0	0	0	---	---	---	---

**Preempt Timing**

Row	Permit	Red Lock	Yellow Lock	Min Recall	Ped Recall	View Set Peds	Rest In Walk	Red Rest	Dual Entry	Max Recall	Soft Recall	Max 2	Cond. Service	Man Cntrl Calls	Yellow Start	First Phases
0																
1																
2																
3																
4																
5																
6																
7																
8																
9																
A																
B																
C																
D																
E																
F																

**Phase Functions** <C+0+F=1>  
 F + F + interval

Row	Column Numbers --->	1	2	3	4	5	6	7	8
0	Overlap Name --->								
1	Load Switch Number	0	0	0	0	0	0	0	0
2	Veh Set 1 - Phases								
3	Veh Set 2 - Phases								
4	Veh Set 3 - Phases								
5	Neg Veh Phases								
6	Neg Ped Phases								
7	Green Omit Phases								
8	Green Clear Omit Phs.								
9									
A									
B									
C									
D	Green Clear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
E	Yellow Change	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
F	Red Clear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Overlap Assignments <C+0+E=29>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
EV-A	0															
EV-B	0															
EV-C	0															
EV-D	0															
RR-1*	---															
RR-2*	---															
SE-1	0															
SE-2	0															

- Extra 1 Flags**  
 1 = TBC Type 1  
 2 = NEMA Ext. Coord  
 3 = Auto Daylight Savings  
 4 = EV Advance  
 5 = Extended Status  
 6 = International Ped  
 7 = Flash - Clear Outputs  
 8 = Split Ring
- Extra 2 Flags**  
 1 = AWB During Initial  
 2 = LMU Installed  
 3 = Disable Min Walk  
 4 = QuickNet/4 System  
 5 = Ignore P/P on EV  
 6 =  
 7 = Reserved  
 8 =

**Preempt Priority**  
 <C+0+E=125>  
 (\* RR-1 is always Highest, and RR-2 is second Highest)

Row	Column Numbers --->	E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot. / Perm Phases	
5	Flash to PE Circuits	
6	Flash Entry Phases	
7	Disable Yellow Range	
8	Disable Ovp Yel Range	
9	Overlap Yellow Flash	
A	EV-A Phases	
B	EV-B Phases	
C	EV-C Phases	
D	EV-D Phases	
E	Extra 1 Config. Bits	1, 3, 5
F	IC Select (Interconnect)	2

**Configuration** <C+0+E=125>  
 E + E + interval

Column Numbers --->	F
Ext. Permit 1 Phases	
Ext. Permit 2 Phases	
Exclusive Ped Assign	
P preempt Non-Lock	12345678
Ped for 2P Output	2
Ped for 6P Output	6
Ped for 4P Output	4
Ped for 8P Output	8
Yellow Flash Phases	
Low Priority A Phases	
Low Priority B Phases	
Low Priority C Phases	
Low Priority D Phases	
Restricted Phases	
Extra 2 Config. Bits	

**Configuration** <C+0+E=125>  
 E + F + interval

Column Numbers --->	F
Fast Green Flash Phase	
Green Flash Phases	
Flashing Walk Phases	
Guaranteed Passage	
Simultaneous Gap Term	12345678
Sequential Timing	
Advance Walk Phases	
Delay Walk Phases	
External Recall	
Start-up Overlap Green	
Max Extension	
Inhibit Ped Reservice	
Semi-Actuated	
Start-up Overlap Yellow	
Start-up Vehicle Calls	12345678
Start-up Ped Calls	2, 4

**Specials** <C+0+F=2>  
 F + F + interval

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Phase 1	10															
Phase 2	10															
Phase 3	10															
Phase 4	10															
Phase 5	10															
Phase 6	10															
Phase 7	10															
Phase 8	10															

- Flash to PE & PE Non-Lock**  
 1 = EV A 5 = RR 1  
 2 = EV B 6 = RR 2  
 3 = EV C 7 = SE 1  
 4 = EV D 8 = SE 2
- IC Select Flags**  
 1 =  
 2 = Modem  
 3 = 7-Wire Slave  
 4 = Flash / Free  
 5 =  
 6 = Simplex Master  
 7 = 7-Wire Master  
 8 = Offset Interrupter

**Coordination Transition Minimums**  
 <C+0+C=5>

INTERSECTION: JUNIPERO SERRA BOULEVARD / SAN PEDRO ROAD

(Coord Extra Bit 1 = Programmed WALK Time for Sync Phases)

Column Numbers	1	2	3	4	5	6	7	8	9
Plan Name	Plan								
Cycle Length	120	110	110		100	120	100	100	100
Phase 1 - ForceOff	17	14	14		67	104	67	68	68
Phase 2 - ForceOff	0	0	0		0	25	0	0	0
Phase 3 - ForceOff	40	47	44		25	59	22	23	28
Phase 4 - ForceOff	90	80	80		55	88	52	53	55
Phase 5 - ForceOff	114	101	101		74	25	77	74	74
Phase 6 - ForceOff	17	13	13		0	0	0	0	0
Phase 7 - ForceOff	40	80	34		18	50	27	25	25
Phase 8 - ForceOff	90	52	80		55	88	52	53	55
Ring Offset									
Offset 1	68	76	47		30	16	19	22	22
Offset 2									
Offset 3									
Perm 1 - End	15	15	15		15	15	15	15	15
Hold Release	255	255	255		255	255	255	255	255
Zone Offset									

<C+0+C=1>

Coordination - Bank 1

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Plan 1 - Sync																
Plan 2 - Sync																
Plan 3 - Sync																
Plan 4 - Sync																
Plan 5 - Sync																
Plan 6 - Sync																
Plan 7 - Sync																
Plan 8 - Sync																
Plan 9 - Sync																
NEMA Sync																
NEMA Hold																
Coord Extra																

Sync Phases <C+0+C=1>

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Ped Adjustment	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Perm 2 - Start	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Perm 2 - End	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Perm 3 - Start	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Perm 3 - End	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Reservice Time	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Reservice Phases																
Pretimed Phases																
Max Recall																
Perm 1 Veh Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
Perm 1 Ped Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
Perm 2 Veh Phase																
Perm 2 Ped Phase																
Perm 3 Veh Phase																
Perm 3 Ped Phase																

<C+0+C=2>

Coordination - Bank 2

Row	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
Free Lag																
Plan 1 - Lag																
Plan 2 - Lag																
Plan 3 - Lag																
Plan 4 - Lag																
Plan 5 - Lag																
Plan 6 - Lag																
Plan 7 - Lag																
Plan 8 - Lag																
Plan 9 - Lag																
External Lag																

Lag Phases <C+0+C=1>

Row	Column 3	Column A	Column B	Column C	Column D	Column E	Column F			
0	Spec. Funct. 1	0	NOT-3	0	Pretimed	0	Dial 2 (7-Wire)	0	Sim Term	0
1	Spec. Funct. 2	0	NOT-4	0	Plan 1	0	Dial 3 (7-Wire)	0	EV-A	71
2	Spec. Funct. 3	0	OR-4 (a)	0	Plan 2	0	Offset 1 (7-Wire)	0	EV-B	72
3	Spec. Funct. 4	0	OR-4 (b)	0	Plan 3	0	Offset 2 (7-Wire)	0	EV-C	73
4	NAND-3 (a)	0	OR-5 (a)	0	Plan 4	0	Offset 3 (7-Wire)	0	EV-D	74
5	NAND-3 (b)	0	OR-5 (b)	0	Plan 5	0	Free (7-Wire)	82	RR-1	51
6	NAND-4 (a)	0	OR-6 (a)	0	Plan 6	0	Flash Sense	81	RR-2	52
7	NAND-4 (b)	0	OR-6 (b)	0	Plan 7	0	Excl. Ped Orbit	0	Spec. Event 1	0
8	OR-7 (a)	0	Fig 3 Diamond	0	Plan 8	0	Man. Advance	0	Spec. Event 2	0
9	OR-7 (b)	0	Fig 4 Diamond	0	Plan 9	0	External Alarm	0	External Lag	0
A	OR-7 (c)	0	Max Inhibit (nema)	0	DELAY-A	0	Phase Bank 2	0	AND-1 (a)	0
B	OR-7 (d)	0	Force A (nema)	0	DELAY-B	0	Phase Bank 3	0	AND-1 (b)	0
C	OR-8 (a)	0	Force B (nema)	0	DELAY-C	0	Overlap Set 2	0	AND-2 (a)	0
D	OR-8 (b)	0	C.N.A. (nema)	0	DELAY-D	0	Overlap Set 3	0	AND-2 (b)	0
E	OR-8 (c)	0	Hold (nema)	0	DELAY-E	0	Detector Set 2	0	AND-3 (a)	0
F	OR-8 (d)	0	Max Recall	0	DELAY-F	0	Detector Set 3	0	AND-3 (b)	0

<C=0+E=126>

Assignable Inputs

Row	Column 5	Column A	Column B	Column C	Column D	Column E	Column F			
0	Phase ON - 1	0	Preempt Fail	0	Free	0	TOD Out 1	0	Dial 2 (7-Wire)	0
1	Phase ON - 2	0	Sp Evt Out 1	0	Plan 1	0	TOD Out 2	0	Dial 3 (7-Wire)	0
2	Phase ON - 3	0	Sp Evt Out 2	0	Plan 2	0	TOD Out 3	0	Offset 1 (7-Wire)	0
3	Phase ON - 4	0	Sp Evt Out 3	0	Plan 3	0	TOD Out 4	0	Offset 2 (7-Wire)	0
4	Phase ON - 5	0	Sp Evt Out 4	0	Plan 4	0	TOD Out 5	0	Offset 3 (7-Wire)	0
5	Phase ON - 6	0	Sp Evt Out 5	0	Plan 5	0	TOD Out 6	0	Free (7-Wire)	0
6	Phase ON - 7	0	Sp Evt Out 6	0	Plan 6	0	TOD Out 7	0	Flash (7-Wire)	0
7	Phase ON - 8	0	Sp Evt Out 7	0	Plan 7	0	TOD Out 8	0	Preempt	0
8	Ph. Check - 1	0	Sp Evt Out 8	0	Plan 8	0	Adv. Warn - 1	0	Low Priority A	0
9	Ph. Check - 2	0	NOT-3	0	Plan 9	0	Adv. Warn - 2	0	Low Priority B	0
A	Ph. Check - 3	0	NOT-4	0	Spec. Funct. 3	0	DELAY-A	0	Low Priority C	0
B	Ph. Check - 4	0	OR-4	0	Spec. Funct. 4	0	DELAY-B	0	Low Priority D	0
C	Ph. Check - 5	0	OR-5	0	NAND-3	0	DELAY-C	0		
D	Ph. Check - 6	0	OR-6	0	NAND-4	0	DELAY-D	0		
E	Ph. Check - 7	0	AND-4	0	OR-7	0	DELAY-E	0		
F	Ph. Check - 8	0	NAND-1	0	OR-8	0	DELAY-F	0		

<C=0+E=127>

Assignable Outputs

Row	Column Numbers -->	Phase							
		1	2	3	4	5	6	7	8
1	Ped Walk	0	7	0	7	0	7	0	7
2	Ped FDW	0	15	0	15	0	15	0	15
3	Min Green	4	7	4	4	4	7	4	4
4	Type 3 Disconnect	0	20	0	20	0	20	0	20
5	Added per Vehicle	0.0	2.0	0.0	2.0	0.0	2.0	0.0	2.0
6	Veh Extension	2.0	4.0	2.0	2.5	2.0	4.0	2.0	2.5
7	Max Gap	3.0	6.0	3.0	3.0	3.0	6.0	3.0	3.0
8	Min Gap	0.5	2.0	0.5	1.5	0.5	2.0	0.5	1.5
9	Max Limit	20	30	20	25	20	30	20	25
10	Max Limit 2	30	50	30	40	30	50	30	40
A	Adv. / Delay Walk	0	0	0	0	0	0	0	0
B	RR Min Ped FDW	7	7	7	7	7	7	7	7
C	Cond Serv Check	10	10	10	10	10	10	10	10
D	Reduce Every	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
E	Yellow Change	3.0	4.0	3.0	3.0	3.0	4.0	3.0	3.0
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

Phase Timing - Bank 2

<C=0+F=2>

Phase	9				A				B				C				D			
	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4
Phase 1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Phase 2	20	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Phase 3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Phase 4	20	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Phase 5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Phase 6	20	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Phase 7	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Phase 8	20	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Initial																				
Alternate Walk																				
Alternate FDW																				
Alternate Initial																				
Alternate Extension																				

Alternate Timing

Row	Column Numbers -->	Phase							
		1	2	3	4	5	6	7	8
1	Ped Walk	0	7	0	7	0	7	0	7
2	Ped FDW	0	15	0	15	0	15	0	15
3	Min Green	4	7	4	4	4	7	4	4
4	Type 3 Disconnect	0	20	0	20	0	20	0	20
5	Added per Vehicle	0.0	2.0	0.0	2.0	0.0	2.0	0.0	2.0
6	Veh Extension	2.0	4.0	2.0	2.5	2.0	4.0	2.0	2.5
7	Max Gap	3.0	6.0	3.0	3.0	3.0	6.0	3.0	3.0
8	Min Gap	0.5	2.0	0.5	1.5	0.5	2.0	0.5	1.5
9	Max Limit	20	30	20	25	20	30	20	25
10	Max Limit 2	30	50	30	40	30	50	30	40
A	Adv. / Delay Walk	0	0	0	0	0	0	0	0
B	RR Min Ped FDW	7	7	7	7	7	7	7	7
C	Cond Serv Check	10	10	10	10	10	10	10	10
D	Reduce Every	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
E	Yellow Change	3.0	4.0	3.0	3.0	3.0	4.0	3.0	3.0
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

Phase Timing - Bank 3

<C=0+F=3>

Phase	9				A				B				C				D			
	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4
Phase 1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Phase 2	20	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Phase 3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Phase 4	20	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Phase 5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Phase 6	20	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Phase 7	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Phase 8	20	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Initial																				
Alternate Walk																				
Alternate FDW																				
Alternate Initial																				
Alternate Extension																				

Alternate Timing

Transition Type 0.3 <C/5+1+9>  
**TBC Transition**

Lag Hold Phases <C/5+1+A>  
**Coordinated Lag Hold Phases**

Sync Output Time 0.0 <C/5+1+C>  
**7-Wire Master**

Begin Month	4	<C/5+2+A>
Begin Week	1	<C/5+2+B>
End Month	10	<C/5+2+C>
End Week	5	<C/5+2+D>

**Daylight Savings Time**

Time Before Yellow	0.0	<F/1+C+E>
Phase Number	0	<F/1+C+F>

**Advance Warning Beacon - Sign 1**

Time Before Yellow	0.0	<F/1+D+E>
Phase Number	0	<F/1+D+F>

**Advance Warning Beacon - Sign 2**

Long Failure	0.7	<F/1+0+6>
Short Failure	0.7	<F/1+0+7>

**Power Cycle Correction (Default = 0.5)**

Min Time (seconds)	0	<F/1+0+8>
--------------------	---	-----------

**Min Green Before PE Force Off**

Max Time (minutes)	255	<F/1+0+9>
--------------------	-----	-----------

**Max Preempt Time Before Failure**

Min Time (seconds)	0	<F/1+0+A>
--------------------	---	-----------

**Min Time Between Same Preempts**  
 (Does Not Apply To Railroad Preempt)

Low Pri. Channel		<E/125+C+8>
------------------	--	-------------

**Disable Low Priority Channel**

Low Priority  
 1 = Channel A  
 2 = Channel B  
 3 = Channel C  
 4 = Channel D

Column Numbers --->

Row	Detector Name	Input Slot	Det. #	0	1	2	3	Delay in sec.	Carry over
0		2/2U	1	39	45 7	2	123 8	0.0	0.0
1		6/2U	2	40	45 7	6	123 8	0.0	0.0
2		4/6U	3	41	45 7	4	123 8	0.0	0.0
3		8/6U	4	42	45 7	8	123 8	0.0	0.0
4		2/2L	5	43	45 7	2	123 8	0.0	0.0
5		6/2L	6	44	45 7	6	123 8	0.0	0.0
6		4/6L	7	45	45 7	4	123 8	0.0	0.0
7		8/6L	8	46	45 7	8	123 8	0.0	0.0
8		2/4	9	47	67	2	123 8	0.0	0.0
9		6/4	10	48	67	6	123 8	0.0	0.0
A		4/8	11	49	67	4	123 8	0.0	0.0
B		8/8	12	50	67	8	123 8	0.0	0.0
C		5/1	13	55	45 7	5	123 8	0.0	0.0
D		1/1	14	56	45 7	1	123 8	0.0	0.0
E		7/5	15	57	45 7	7	123 8	0.0	0.0
F		3/5	16	58	45 7	3	123 8	0.0	0.0

Column Numbers --->	1	2	3	4	5	6	7	8	Row
Walk	0	0	0	0	0	0	0	0	0
Don't Walk	0	0	0	0	0	0	0	0	0
Phase Green	0	0	0	0	0	0	0	0	0
Phase Yellow	0	0	0	0	0	0	0	0	0
Phase Red	0	0	0	0	0	0	0	0	0
Overlap Green	0	0	0	0	0	0	0	0	0
Overlap Yellow	0	0	0	0	0	0	0	0	0
Overlap Red	0	0	0	0	0	0	0	0	0

**Redirect Phase Outputs** <C+0+E=127>

Cabinet Type	0	<E/125+D+0>
<b>Enable Redirection</b> (Enable Redirection = 30)		
Max OFF (minutes)	20	<D/0+0+1>
Max ON (minutes)	7	<D/0+0+2>

**Detector Failure Monitor**

Row	Detector Name	Input Slot	Det. #	4	5	6	7	Delay in sec.	Carry over
0		5/9U	17	59	45 7	5	123 8	0.0	0.0
1		1/9U	18	60	45 7	1	123 8	0.0	0.0
2		7/9L	19	61	45 7	7	123 8	0.0	0.0
3		3/9L	20	62	45 7	3	123 8	0.0	0.0
4		2/9U	21	63	45 7	2	123 8	0.0	0.0
5		6/9U	22	64	45 7	6	123 8	0.0	0.0
6		4/7U	23	65	45 7	4	123 8	0.0	0.0
7		8/7U	24	66	45 7	8	123 8	0.0	0.0
8			25	67	2	2	123 8	0.0	0.0
9			26	68	2	6	123 8	0.0	0.0
A			27	69	2	4	123 8	0.0	0.0
B			28	70	2	8	123 8	0.0	0.0
C		2/9L	29	76	45 7	2	123 8	0.0	0.0
D		6/9L	30	77	45 7	6	123 8	0.0	0.0
E		4/7L	31	78	45 7	4	123 8	0.0	0.0
F		8/7L	32	79	45 7	8	123 8	0.0	0.0

**Detector Assignments**

<C+0+E=126>

Row	0	1	2	3	4	5	6	7
Output Port 1								
Output Port 2								
Output Port 3								
Output Port 4								
Output Port 5								
Output Port 6								
Output Port 7								

**Dimming** <C+0+E=125>

Row	A	B	C	D	E	F
DELAY-A	0					
DELAY-B	0					
DELAY-C	0					
DELAY-D	0					
DELAY-E	0					
DELAY-F	0					

**Delay Logic Times**

<C+0+D=0> (seconds)

Omit Alarm <C/5+F+0>

**Disable Alarm Reporting**

Time 10 <C/5+C+0>

Redial Time (minutes)

Dial-Back Telephone Number <C+0+C=5>

**Detector Attributes**

1 = Full Time Delay

2 = Ped Call

3 = Count

4 = Extension

5 = Type 3

6 = Calling

7 = Alternate

8 = Alternate

**Det. Assignments**

1 = Det. Set 1

2 = Det. Set 2

3 = Det. Set 3

4 = Failure - Min

5 = Failure - Max

6 = Failure - Min

7 = Failure - Max

8 = Report on Failure

Recall

Recall

8 = Report on Failure

<C+0+D=0>

Row	Time	Plan	Offset	Day of Week
0	07:00	2	A	23456
1	11:00	3	A	23456
2	15:30	E	A	23456
3	00:01	E	A	1 7
4	00:00	0	0	
5	00:00	0	0	
6	00:00	0	0	
7	00:00	0	0	
8	00:00	0	0	
9	00:00	0	0	
A	00:00	0	0	
B	00:00	0	0	
C	00:00	0	0	
D	00:00	0	0	
E	00:00	0	0	
F	00:00	0	0	

TOD Coordination <C+0+9=0.1>

(Bank 1)

Row	Time	Unit	Day of Week	Column 4 Phases/Bits
0	07:00	3	23456	1 4 8
1	15:30	3	23456	1
2	18:30	E	23456	4 8
3	07:00	B	23456	2 4 6 8
4	15:30	B	23456	4 8
5	18:30	B	23456	4 8
6	15:30	3	23456	
7	18:30	3	23456	
8	00:00	0		
9	00:00	0		
A	00:00	0		
B	00:00	0		
C	00:00	0		
D	00:00	0		
E	00:00	0		
F	00:00	0		

TOD Function <C+0+7=1>

<C+0+E=27>

Row	Day	Year	Month	Holiday Type
0	00	00	0	
1	00	00	0	
2	00	00	0	
3	00	00	0	
4	00	00	0	
5	00	00	0	
6	00	00	0	
7	00	00	0	
8	00	00	0	
9	00	00	0	
A	00	00	0	
B	00	00	0	
C	00	00	0	
D	00	00	0	
E	00	00	0	
F	00	00	0	

Holiday Dates <C+0+8=1.1>

(Bank 1)

Row	Time	Plan	Offset	Holiday Type
0	00:00	0	0	
1	00:00	0	0	
2	00:00	0	0	
3	00:00	0	0	
4	00:00	0	0	
5	00:00	0	0	
6	00:00	0	0	
7	00:00	0	0	
8	00:00	0	0	
9	00:00	0	0	
A	00:00	0	0	
B	00:00	0	0	
C	00:00	0	0	
D	00:00	0	0	
E	00:00	0	0	
F	00:00	0	0	

Holiday Events <C+0+9=1.1>

(Bank 1)

- I.O.D. Functions
- 0 =
  - 1 = Red Lock
  - 2 = Yellow Lock
  - 3 = Veh Min Recall
  - 4 = Ped Recall
  - 5 =
  - 6 = Rest In Walk
  - 7 = Red Rest
  - 8 = Double Entry
  - 9 = Veh Max Recall
  - A = Veh Soft Recall
  - B = Maximum 2
  - C = Conditional Service
  - D = Free Lag Phases
  - E = Bit 1 - Local Override
- Bit 4 - Disable Detector  
OFF Monitor  
Bit 7 - Detector Count  
Monitor  
Bit 8 - Real Time Split  
Monitor

- Plan Select
- 1 thru 9 = Coordination  
Plan 1 thru 9
  - 14 or E = Free
  - 15 or F = Flash

- Offset Select
- A = Offset A
  - B = Offset B
  - C = Offset C

- Month Select
- 1 = January
  - 2 = February
  - 3 = March
  - 4 = April
  - 5 = May
  - 6 = June
  - 7 = July
  - 8 = August
  - 9 = September
  - A = October
  - B = November
  - C = December

Row	Day	Year	Month	Holiday Type
0	00	00	0	
1	00	00	0	
2	00	00	0	
3	00	00	0	
4	00	00	0	
5	00	00	0	
6	00	00	0	
7	00	00	0	
8	00	00	0	
9	00	00	0	
A	00	00	0	
B	00	00	0	
C	00	00	0	
D	00	00	0	
E	00	00	0	
F	00	00	0	

Holiday Dates <C+0+8=1.2>

(Bank 2)

Row	Time	Plan	Offset	Holiday Type
0	00:00	0	0	
1	00:00	0	0	
2	00:00	0	0	
3	00:00	0	0	
4	00:00	0	0	
5	00:00	0	0	
6	00:00	0	0	
7	00:00	0	0	
8	00:00	0	0	
9	00:00	0	0	
A	00:00	0	0	
B	00:00	0	0	
C	00:00	0	0	
D	00:00	0	0	
E	00:00	0	0	
F	00:00	0	0	

Holiday Events <C+0+9=1.2>

(Bank 2)

Row	Time	Plan	Offset	Day of Week
0	00:00	0	0	
1	00:00	0	0	
2	00:00	0	0	
3	00:00	0	0	
4	00:00	0	0	
5	00:00	0	0	
6	00:00	0	0	
7	00:00	0	0	
8	00:00	0	0	
9	00:00	0	0	
A	00:00	0	0	
B	00:00	0	0	
C	00:00	0	0	
D	00:00	0	0	
E	00:00	0	0	
F	00:00	0	0	

TOD Coordination <C+0+9=0.2>

(Bank 2)

Row	Time	Unit	Day of Week	Column 4 Phases/Bits
0	:00	0		
1	00:00	0		
2	00:00	0		
3	00:00	0		
4	00:00	0		
5	00:00	0		
6	00:00	0		
7	00:00	0		
8	00:00	0		
9	00:00	0		
A	00:00	0		
B	00:00	0		
C	00:00	0		
D	00:00	0		
E	00:00	0		
F	00:00	0		

Holiday <C+0+7=2>

TOD Function <C+0+E=28>



